## **ACKNOWLEDGEMENT RECEIPT**

**Electronic Version** 

Stylesheet Version v01

Title of Invention

METHOD OF FABRICATING AN INTEGRATED CIRCUIT THROUGH UTILIZING METAL LAYERS TO PROGRAM RANDOMLY POSITIONED BASIC UNITS

Submision Type: Utility Patent Filing

Application Number:

10/708256

EFS ID: 55822

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First Named Applicant: Hsin-Shih Wang
Attorney Docket Number: FTCP0031USA

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File Listing:

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us-power-of-attorney-grant	FTCP0031-uspoat.xml	1045	2004-02-19
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